Super-scalar

Socket

R1
R2

... 

Fetch

Decode

E1
E2
E3
E4

Node

RAM

Front-Side Bus
Hyper-threading

Socket

Fetch

Decode

E1

E2

E3

E4

R1'

R2'

R1

R2

. . .

Node

RAM

. . .

Front-Side Bus

Note: Intel only
Multi-Core

Socket

Typically 2 to 8 cores
Symmetric Multiprocessing (SMP)

Note: Can also be multi-core sockets
Cluster: Constellation

Note: Can also be multi-core nodes

Switch