## **GPU Computing Ecosystem**

### CUDA 5 Enterprise level GPU Development

GPU Development Paths Libraries, Directives, Languages

**GPU Tools** Tools, libraries and plug-ins for GPU codes





**3x Single Precision** 

1.8x Memory Bandwidth

Image, Signal, Seismic

Available Now

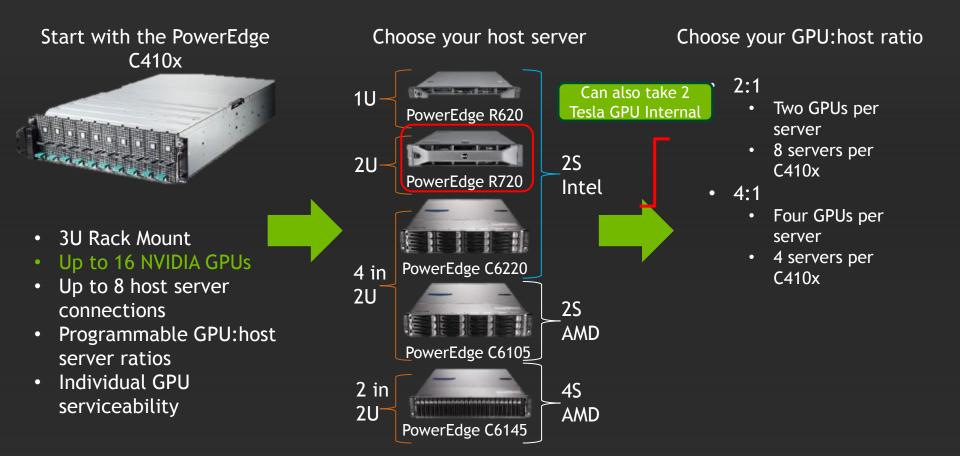
**3x Double Precision** 

Hyper-Q, Dynamic Parallelism

CFD, FEA, Finance, Physics

Available Q4 2012

# Add GPU Processing Your Way



# Introducing CUDA 5

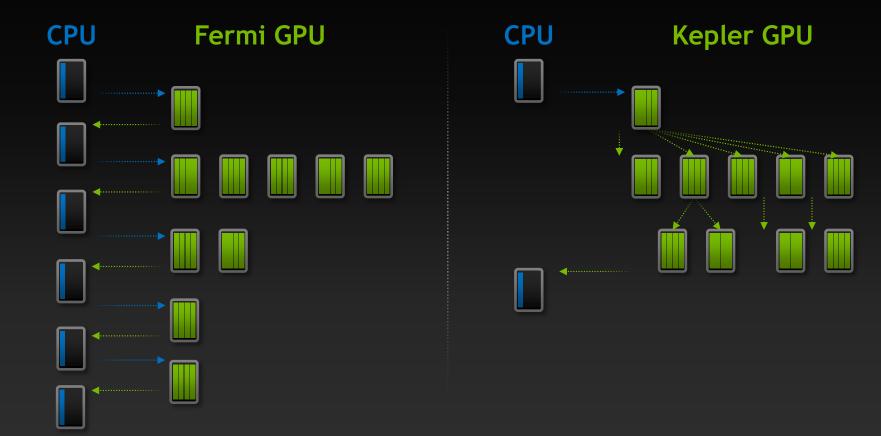
# **CUDA** By the Numbers:



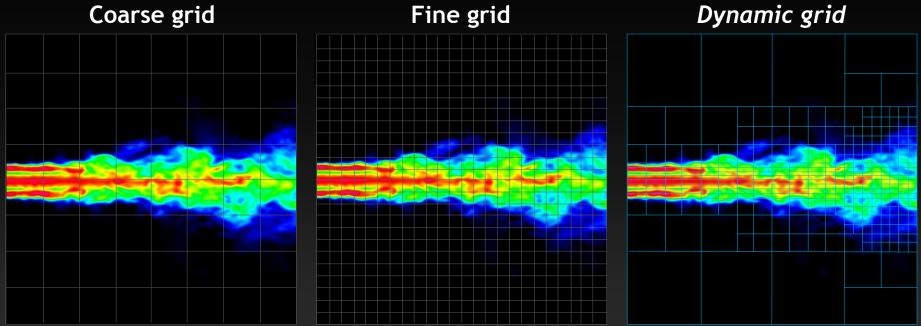
>120,000 Active Developers

>500 Universities Teaching CUDA

# **Dynamic Parallelism**



## **Dynamic Work Generation**



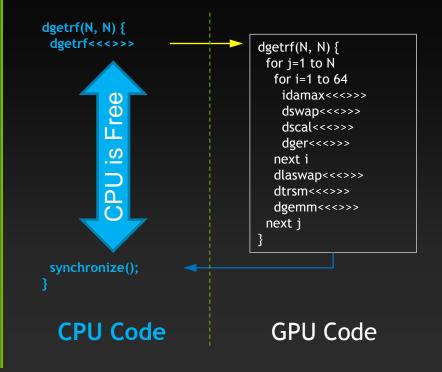
Higher Performance Lower Accuracy Lower Performance Higher Accuracy Target performance where accuracy is required

Supported on GK110 GPUs

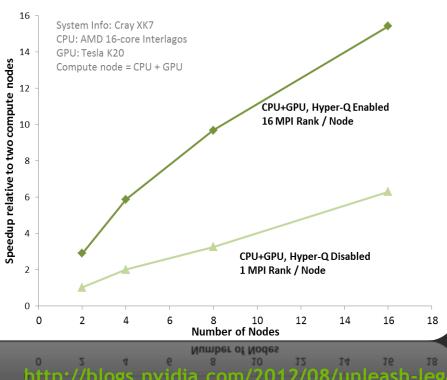
## Simpler Code: LU Example

#### LU decomposition (Fermi) dgetrf(N, N) { for j=1 to N for i=1 to 64 idamax<<<>>>> idamax(); memcpy dswap(); dswap<<<>>>> memcpy dscal(); dscal<<<>>>> dger<<<>>>> dger(); next i memcpy dlaswap(); dlaswap<<<>>> dtrsm<<<>>>> dtrsm(); dgemm<<<>>>> dgemm(); CPU Code **GPU** Code

### LU decomposition (Kepler)



## Easy Speed-up for MPI Codes with Hyper-Q



### **CP2K Success Story**

- M2090 GPU shows no performance improvement over CPUs
- Like most HPC codes, MPI jobs are too small for GPUs
- Hyper-Q provides 2.5x speed-up without major code rewrite

http://blogs.nvidia.com/2012/08/unleash-legacy-mpi-codes-with-keplers-hyper-q/

**GPU Development Paths** 

GPUs have evolved to the point where many real-world applications are easily implemented on them and run significantly faster than on multi-core systems.

Future computing architectures will be hybrid systems with parallel-core GPUs working in tandem with multi-core CPUs\_\_\_\_\_



#### Jack Dongarra

Professor, University of Tennessee Director of the Innovative Computing Laboratory Author of LINPACK

# Small Changes, Big Speed-up

### **Application Code**



# **Commercial Apps Accelerated by GPUs**

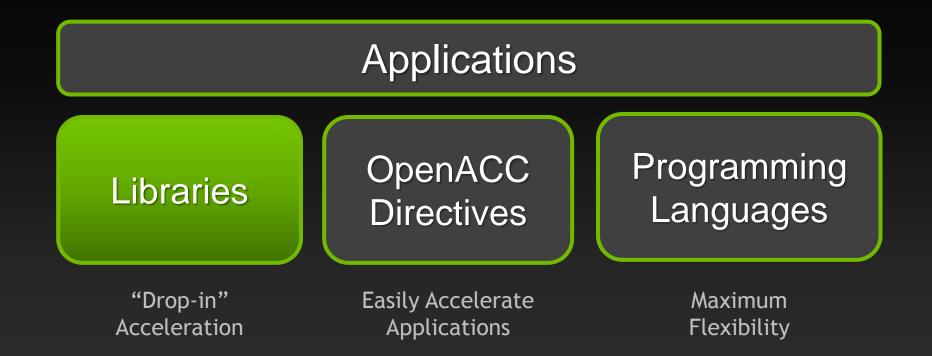
Molecular Dynamics	AMBER • CHARMM • DL_POLY • GAMESS-US • GROMACS LAMMPS • NAMD
Fluid Dynamics	Altair Acusolve • Autodesk Moldflow • OpenFOAM Prometech Particlework • Turbostream
Earth Sciences	ASUCA • HOMME • NASA GEOS-5 • NOAA NIM • WRF
Engineering Simulation	Agilent EMPro • ANSYS Mechanical • ANSYS Nexxim • CST Microwave Studio Impetus AFEA • Remcom XFdtd • SIMULIA Abaqus
Others	GADGET2 • MATLAB • Mathematica • NBODY • Paradigm VoxelGeo PARATEC • Schlumberger Petrel

### **Rapidly Growing GPU-Accelerated Application Catalog**

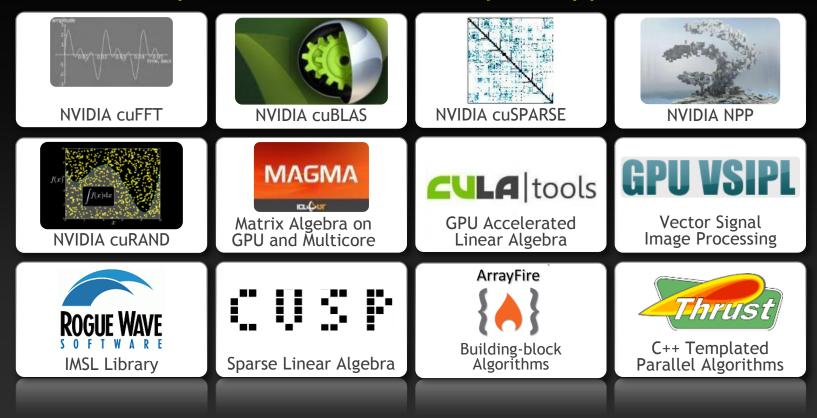
#### POPULAR GPU-ACCELERATED APPLICATIONS

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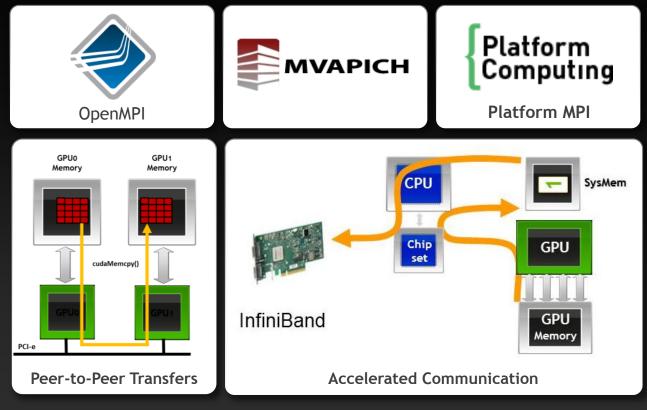
# **3 Ways to Accelerate Applications**



### **GPU Accelerated Libraries** "Drop-in" Acceleration for your Applications

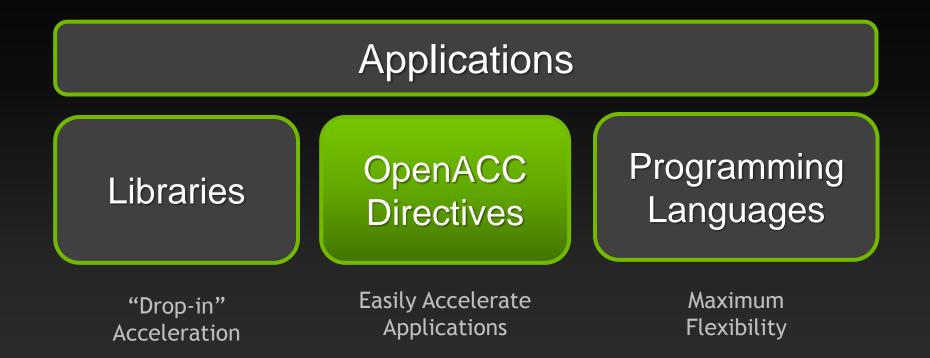


### GPU-Aware MPI Libraries Integrated Support for GPU Computing



developer.nvidia.com/gpudirect

# **3 Ways to Accelerate Applications**



# OpenACC

### **Open Programming Standard for Parallel Computing**

"OpenACC will enable programmers to easily develop portable applications that maximize the performance and power efficiency benefits of the hybrid CPU/GPU architecture of Titan."

--Buddy Bland, Titan Project Director, Oak Ridge National Lab

"OpenACC is a technically impressive initiative brought together by members of the OpenMP Working Group on Accelerators, as well as many others. We look forward to releasing a version of this proposal in the next release of OpenMP."

--Michael Wong, CEO OpenMP Directives Board







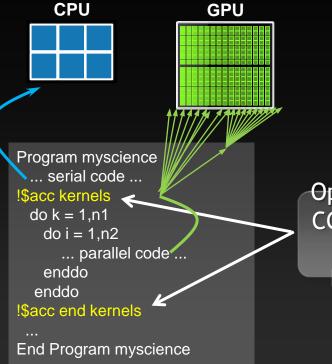








# **OpenACC Directives**



Your original Fortran or C code OpenAC CCompi ler Hint Simple Compiler hints

Compiler Parallelizes code

Works on many-core GPUs & multicore CPUs



DIRECTIVES FOR ACCELERATORS

www.nvidia.com/gpudirectives

# Directives: Easy & Powerful

#### Real-Time Object Detection

Global Manufacturer of Navigation Systems



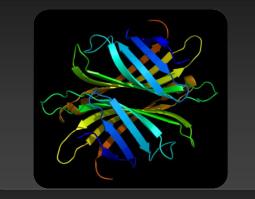
### Valuation of Stock Portfolios using Monte Carlo

Global Technology Consulting Company



### Interaction of Solvents and Biomolecules

University of Texas at San Antonio

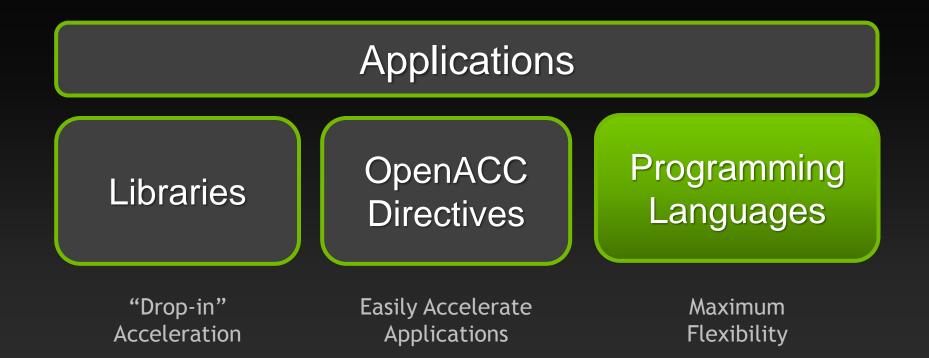


# 5x in 40 Hours2x in 4 Hours5x in 8 Hours

Optimizing code with directives is quite easy, especially compared to CPU threads or writing CUDA kernels. The most important thing is avoiding restructuring of existing code for production applications.

-- Developer at the Global Manufacturer of Navigation Systems

# **3 Ways to Accelerate Applications**



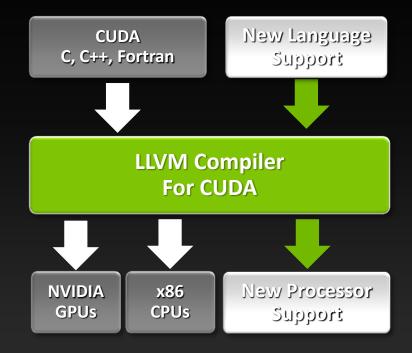
## **Opening the CUDA Platform with LLVM**

CUDA compiler source contributed to open source LLVM compiler project

SDK includes specification documentation, examples, and verifier

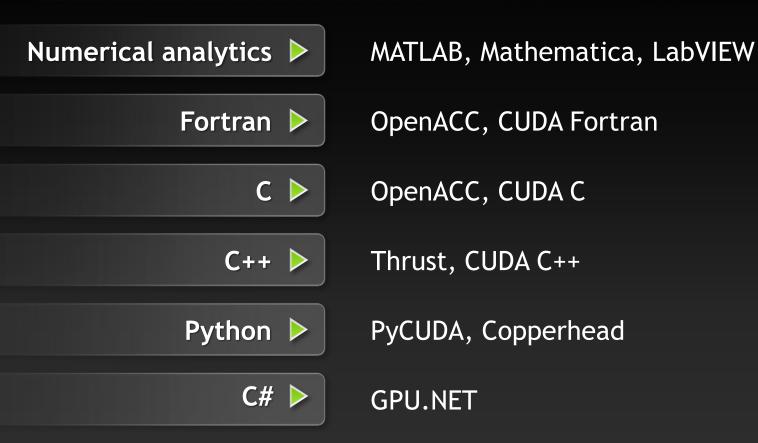
Provides ability for anyone to add CUDA to new languages and processors

> Learn more at developer.nvidia.com/cuda-source





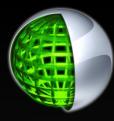
# **GPU Programming Languages**



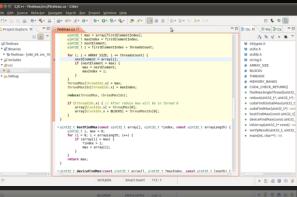


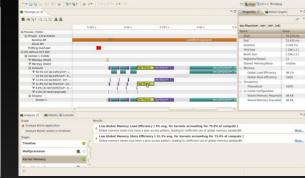
# Development Tools

### NVIDIA<sup>®</sup> Nsight<sup>™</sup> Eclipse Edition for Linux and MacOS



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#### **CUDA-Aware Editor**

Automated CPU to GPU code refactoring

Semantic highlighting of CUDA code Integrated code samples & docs Nsight Debugger

Simultaneously debug CPU and GPU
 Inspect variables across CUDA threads

Use breakpoints & single-step debugging

#### **Nsight Profiler**

Quickly identifies performance issues

- Integrated expert system
- Source line correlation

developer.nvidia.com/nsight

Debugging Solutions Command Line to Cluster-Wide



## Performance Analysis Tools Single Node to Hybrid Cluster Solutions



developer.nvidia.com/performance-analysis-tools

# Job Scheduling & Cluster Management



developer.nvidia.com/cuda-tools-ecosystem

# **GPU Test Drive**



Experience the Acceleration

Program that provides free access to a remote/cloud GPU cluster

Who 🕨 For academic researchers

What 🕨

To experience how applications accelerate with GPUs

**Benefit to Researchers** 

FREE & EASY way to start with GPUs

No GPU programming expertise needed

Access to a remote, pre-configured GPU cluster for evaluation

### www.nvidia.com/GPUTestDrive

# GPU Technology Conference 2013 March 18-21 | San Jose, CA

### **Reasons to attend GTC**

- Learn about leading-edge advances in GPU computing
- Explore the research as well as the commercial applications
- Discover advances in computational visualization
- Take a deep dive into parallel programming

### Ways to participate

- Submit a Research Poster share your work and gain exposure as a thought leader
- Register learn from the experts and network with your peers
- Exhibit/Sponsor promote your organization as a key player in the GPU ecosystem
   Visit www.gputechconf.com





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